A1 Cont'd

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.

Please add new claim 24 as follows:

A2

24. {NEW} An active-matrix-addressed liquid crystal display device comprising:

a substrate, on which the thin-film transistor according to claim 14; a data bus line electrically connected to the first heavily doped region of the thin-film transistor; a gate bus line electrically connected to at least one of the gate electrodes of the thin-film transistor; and a pixel electrode electrically connected to the second heavily doped region of the thin-film transistor have been formed, and

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.

REMARKS

The above amendments are made to place the claims in a more traditional format.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page(s) is captioned "**Version With Markings To Show Changes Made**."

Respectfully submitted,

NIXON & VANDERHYE P.C.

December 18, 2001

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

23. {AMENDED} An active-matrix-addressed liquid crystal display device comprising:

a substrate, on which the thin-film transistor according to claim 1 [or 14]; a data bus line electrically connected to the first heavily doped region of the thin-film transistor; a gate bus line electrically connected to at least one of the gate electrodes of the thin-film transistor; and a pixel electrode electrically connected to the second heavily doped region of the thin-film transistor have been formed, and

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.